

COLLEGE OF ENGINEERING, MALEGAON (BK)
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION
MASTER TIMETABLE SEM-I (2017-18)

SEM-I

W.E.F. 14/08/2017

COLLEGE OF ENGINEERING, MALEGAON (BK)
 DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION

TIME	CLASS	8.30-9.30	9.30-10.30	10.45-11.45	11.45-12.45	1.30-2.30	2.30-3.30
DAY							
MON	S.E (A)	SS (SBD)	DE (SNK)	DSA (RDT)	EDC (AVH)	ECM/EMIT/DE A3(KAT)/ A2(SDK) / A3(SNK)	
	T.E (A)	DSP (YVP)	MT (SNY)	EM (DSG)	MC (SGG)	MT/DC/MC A2(SNY) /A3(RDT)/A1(TNP)	
	T.E (B)	MC /MT B1(RKP)/B2(BSK)		DSP /DC B1(SDK) / B2(AAP)/ B3(BSK)	MT (DC)	EM (SPS)	
	B.E(A)	MWE (MNK)	ES&RTOS (JHS)	PLC (RKP)	VLSI (NAD)	MWE/ VLSI / ES&RTOS/CN A1(MNK) / A2 (YVP) / A3(JHS)/A4(DBK)	
	B.E(B)	MWE (DSG)	CN (SGG)	ES&RTOS (DBK)	PLC (SNY)	PROJECT WORK	
TUE	S.E (A)	DSA (RDT)	SS (SBD)	EDC (AVH)	ECM (MNK)	EDC/ECM/DE/DSA A2(AVH) / A3(KAT) /A1(SNK)/A4(RDT)	
	T.E (A)	DC (BSK)	MT (SNY)	DSP (YVP)	EM (DSG)	MT /DC A3(SNY)/ A1(BSK)	
	T.E (B)	DSP (SDK)	ESD (AVH)	DSP /DC B2(SDK) / B3(RDT)/B1(BSK)	MT (DC)	EM (SPS)	MC (RKP)
	B.E(A)	CN (SPB)	MWE (MNK)	PLC (RKP)	ES&RTOS (JHS)	MWE/ VLSI/ ES&RTOS/CN A1 / A2 / A3 /A4	
	B.E(B)	MWE /VLSI / ES&RTOS /CN B2(DSG) /B3(SPS) / B4 (DBK)/B1(SGG)		VLSI (SPS)	PLC (SNY)	ES&RTOS (DBK)	CN (SGG)
WED	S.E (A)	EDC/DE/DSA A3 / A2 / A1		SS (SBD)	DE (SNK)	EDC/EMIT /DSA/DE A1(KAT) / A3 (SDK) / A2(RDT) / A4(AVH)	
	T.E (A)	MC /ESD /DSP A2(RKP)/ A1(KGJ)/A3(YVP)		MC (SGG)	DC (BSK)	MT /ESD /DC A1(SNY) /A3 (KGJ)/ A2(BSK)	
	T.E (B)	MT (BSK)	DSP (SDK)	DSP /DC B3(SDK) / B1(RDT) / B2(AVH)	ESD (DC)	EM (SPS)	
	B.E(A)	CN (SPB)	VLSI (NAD)	MWE (MNK)	PLC (RKP)	MWE /VLSI / ES&RTOS /CN A3(MNK)/A4 (TNP)/A1 (JHS) /A2(DBK)	
	B.E(B)	MWE /VLSI /ES&RTOS /CN B3(DSG) / B4(SPS) /B1(JHS) /B2(SGG)		VLSI (SPS)	PLC (SNY)	MWE (DSG)	CN (SGG)
THUR	S.E (A)	EDC (AVH)	DSA (RDT)	DE (SNK)	ECM (MNK)	EMIT/ ECM A1 / A2	
	T.E (A)	DSP (YVP)	DC (BSK)	MC (SGG)	ESD (AAP)	DSP /MC A2(YVP) / A3(SGG)	
	T.E (B)	DC (AAP)	MC (RKP)	ESD /MC B1(AVH) /B3(RKP)		MT (BSK)	DSP (SDK)
	B.E(A)	ES&RTOS (JHS)	VLSI (NAD)	MWE (MNK)	CN (SPB)	MWE /VLSI / ES&RTOS/CN A4(MNK)/A1(NAD)/A2(TNP) /A3(DBK)	
	B.E(B)	MWE /VLSI / ES&RTOS /CN B4(DSG)/B1(SPS) /B2 (DBK) /B3(SGG)		PLC (SNY)	ES&RTOS (DBK)	VLSI (SPS)	MWE (DSG)
FRI	S.E (A)	ECM (MNK)	SS (SBD)	DSA (RDT)	EMIT (SNK)	EMIT/DSA/ECM A4 /A3 / A1	
	T.E (A)	ESD (AAP)	MT (SNY)	EM (DSG)	DC (BSK)	DSP /ESD A1(SDK) / A2(KGJ)	
	T.E (B)	ESD (AVH)	MT (BSK)	ESD /MC B3(AVH) / B2(RKP)		DC (AAP)	MC (RKP)
	B.E(A)	PLC (RKP)	ES&RTOS (JHS)	CN (SPB)	VLSI (NAD)	MWE /VLSI /ES&RTOS /CN A2(MNK)/A3(TNP)/A4(JHS) /A1(DBK)	
	B.E(B)	MWE /VLSI / ES&RTOS /CN B1(DSG)/B2(TNP)/B3(DBK) /B4(SGG)		ES&RTOS (DBK)	VLSI (SPS)	CN (SGG)	MWE (DSG)
SAT	S.E (A)	DE (SNK)	DE (SNK)	ECM (MNK)	ECM (MNK)	EDC (AVH)	EDC (AVH)
	T.E (A)	EM (DSG)	EM (DSG)	DSP (YVP)	DSP (YVP)	MT (SNY)	MT (SNY)
	T.E (B)	DSP (SDK)	DSP (SDK)	EM (SPS)	EM (SPS)	MT (BSK)	MT (BSK)
	B.E(A)	PROJECT					
	B.E(B)	PROJECT					
Note: SAS (Tut): Monday -A4(SBD)-1.30-2.30, Wednesday -A1-(SBD)-1.30-2.30 Thursday :A3-(SBD)-1.30-2.30, Friday : A2(SBD)-1.30-2.30 EM (Tut): Tuesday A2(DSG)-1.30-2.30, Thursday A1-(DSG)-1.30-2.30 , Friday :A3-(DSG)- 1.30-2.30 EM (Tut): Thursday B2(SPS)-10.45-11.45, Monday B3-(SPS)-9.30-10.30 , Friday :B1-(SPS)- 10.45-11.45							

HOD
(Prof.S.B.Dixit)

PRINCIPAL
(Dr. D.M.Yadav)